

TOSHIBA TRANSISTOR SILICON NPN TRIPLE DIFFUSED MESA TYPE

2SD2499

HORIZONTAL DEFLECTION OUTPUT FOR COLOR TVs

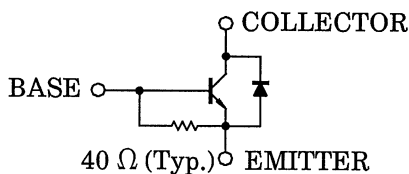
- High Voltage : $V_{CBO} = 1500\text{ V}$
- Low Saturation Voltage : $V_{CE(sat)} = 5\text{ V (Max.)}$
- High Speed : $t_f = 0.3\text{ }\mu\text{s (Typ.)}$
- Built-in Damper Type
- Collector Metal (Fin) is Fully Covered with Mold Resin.

ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$)

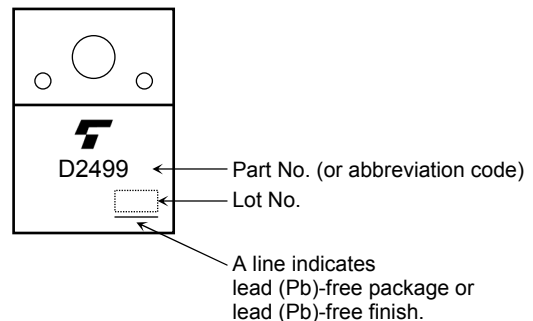
CHARACTERISTIC		SYMBOL	RATING	UNIT
Collector-Base Voltage		V_{CBO}	1500	V
Collector-Emitter Voltage		V_{CEO}	600	V
Emitter-Base Voltage		V_{EBO}	5	V
Collector Current	DC	I_C	6	A
	Pulse	I_{CP}	12	
Base Current		I_B	3	A
Collector Power Dissipation		P_C	50	W
Junction Temperature		T_j	150	$^\circ\text{C}$
Storage Temperature Range		T_{stg}	-55~150	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

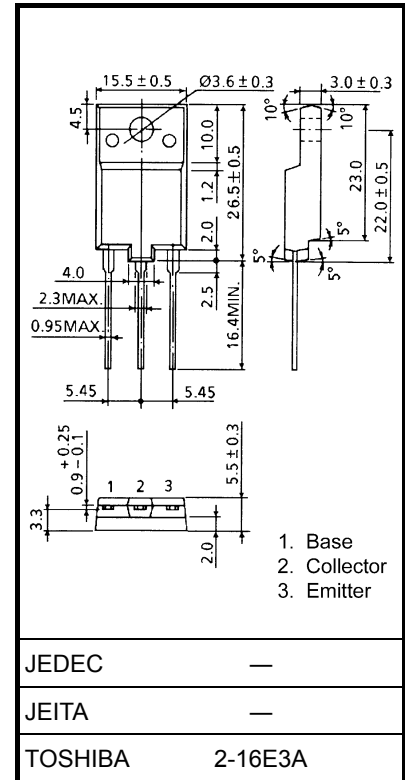
EQUIVALENT CIRCUIT



MARKING



Unit: mm



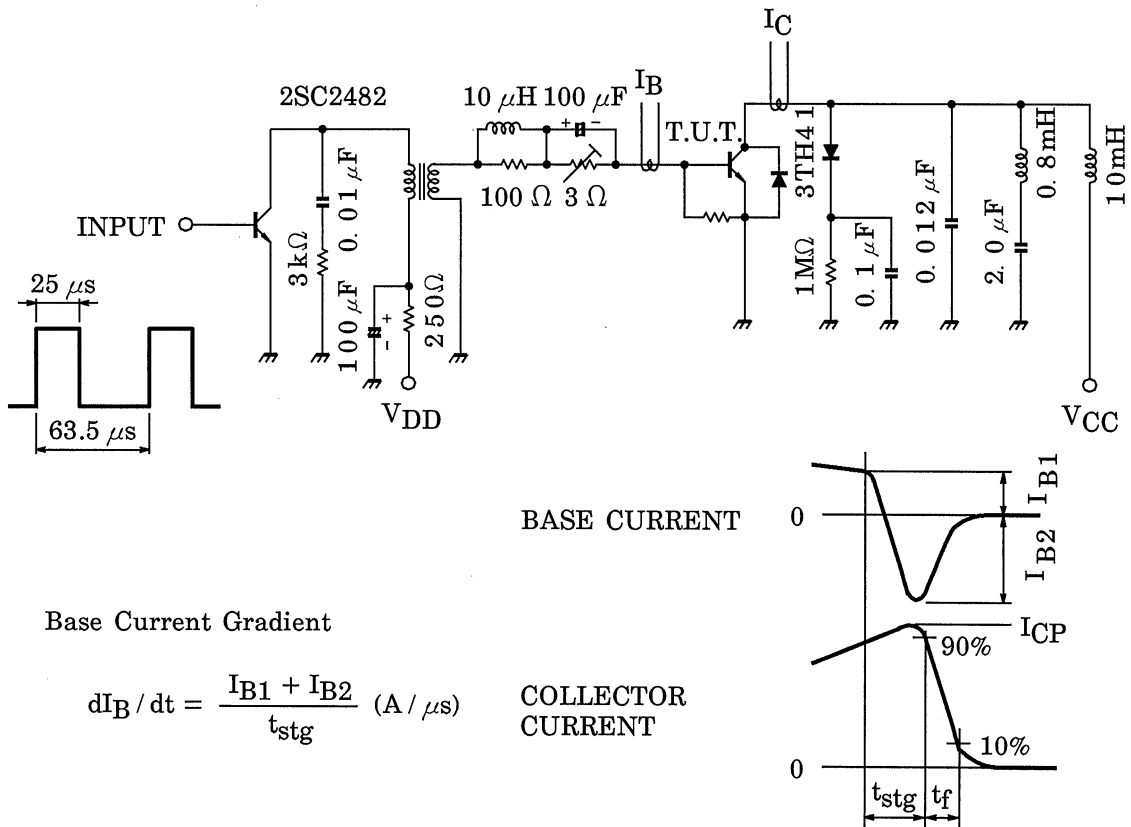
Weight: 5.5 g (typ.)

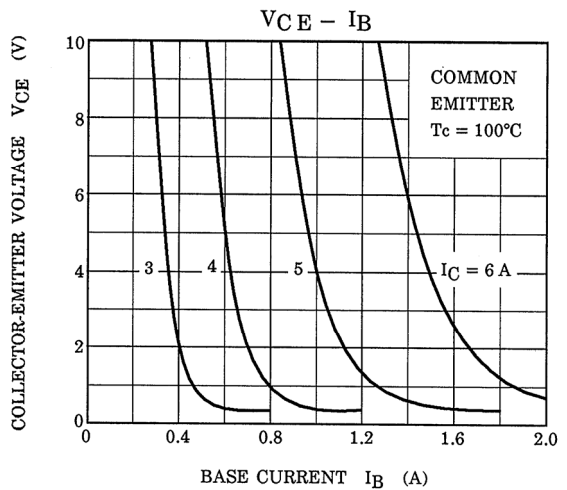
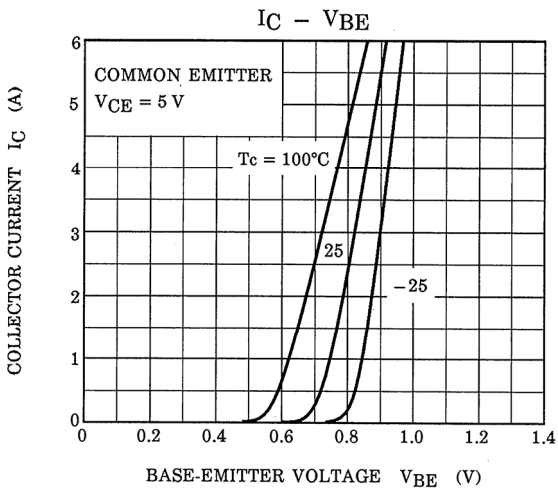
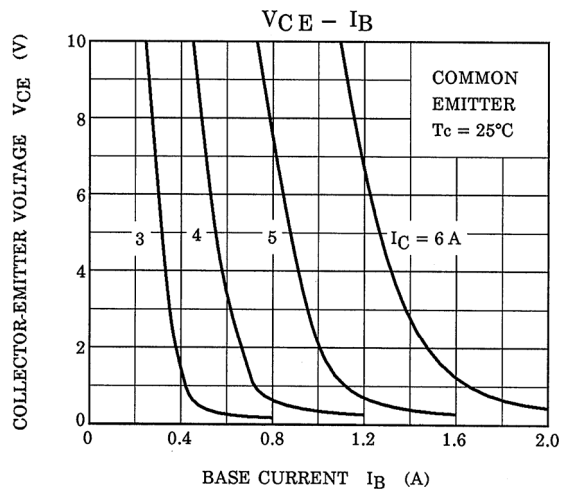
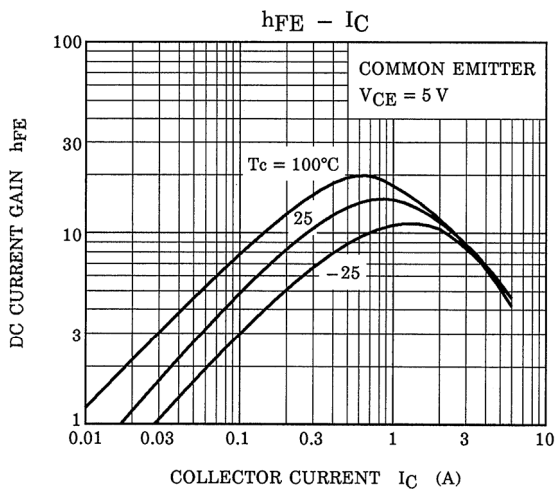
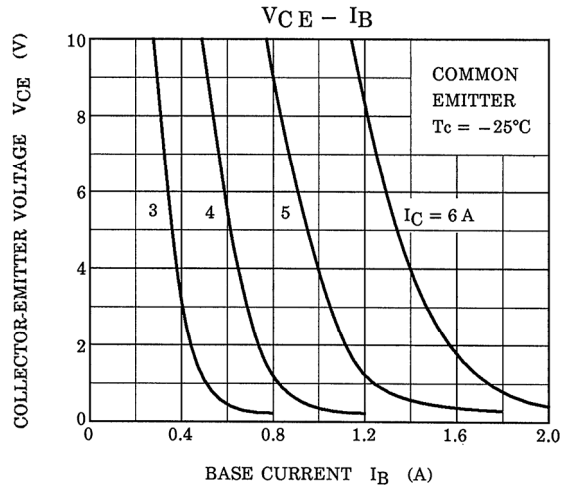
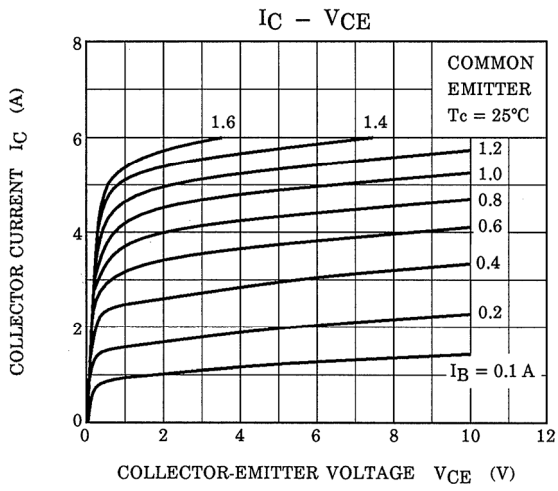
JEDEC	—
JEITA	—
TOSHIBA	2-16E3A

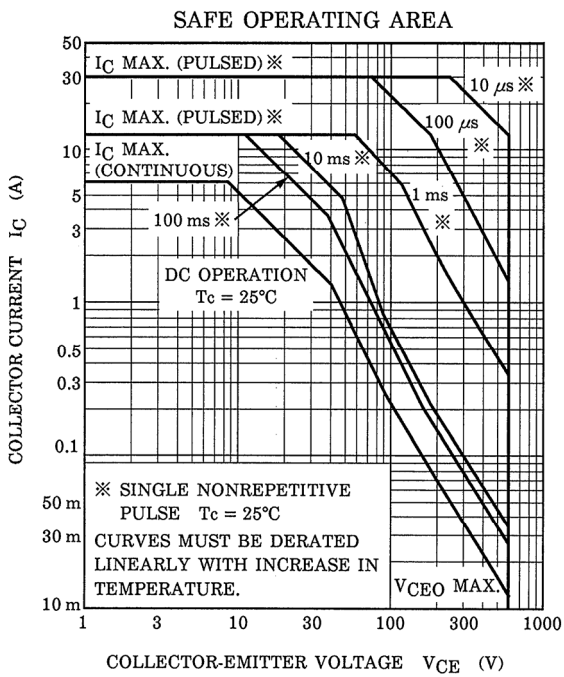
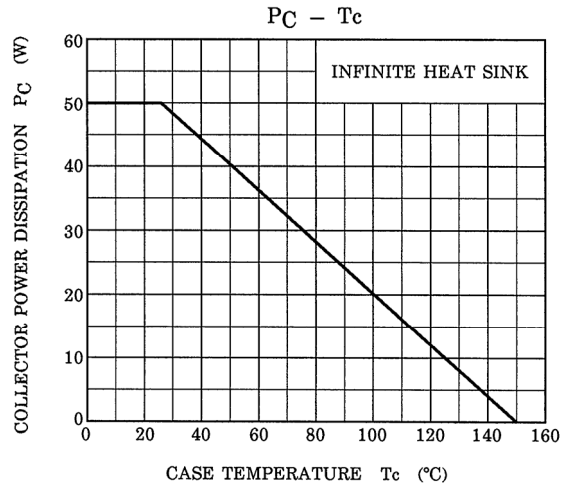
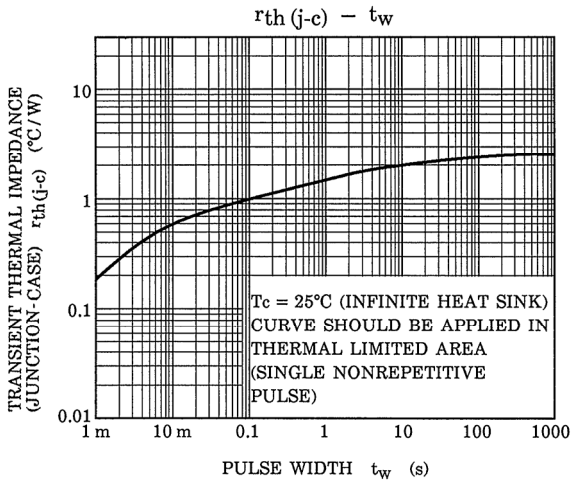
ELECTRICAL CHARACTERISTICS (T_c = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Collector Cut-off Current	I _{CBO}	V _{CB} = 1500 V, I _E = 0	—	—	1	mA
Emitter Cut-off Current	I _{EBO}	V _{EB} = 5 V, I _C = 0	67	—	200	mA
Emitter-Base Breakdown Voltage	V _{(BR) EBO}	I _C = 400 mA, I _B = 0	5	—	—	V
DC Current Gain	h _{FE} (1)	V _{CE} = 5 V, I _C = 1 A	8	—	25	—
	h _{FE} (2)	V _{CE} = 5 V, I _C = 4 A	5	—	9	
Collector-Emitter Saturation Voltage	V _{CE (sat)}	I _C = 4 A, I _B = 0.8 A	—	—	5	V
Base-Emitter Saturation Voltage	V _{BE (sat)}	I _C = 4 A, I _B = 0.8 A	—	1.05	1.3	V
Forward Voltage (Damper Diode)	V _F	I _F = 6 A	—	1.6	2.0	V
Transition Frequency	f _T	V _{CE} = 10 V, I _C = 0.1 A	—	2	—	MHz
Collector Output Capacitance	C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	—	95	—	pF
Switching Time (Fig. 1)	Storage Time	I _{CP} = 4 A, I _{B1} (end) = 0.8 A f _H = 15.75 kHz	—	7.5	11	μs
	Fall Time		—	0.3	0.6	

Fig. 1 SWITCHING TIME TEST CIRCUIT







RESTRICTIONS ON PRODUCT USE

20070701-EN

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